Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1		execut\$3 and ((first test or test script\$1) same (mixed signal semiconductor device or mixed signal integrated circuit))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/13 17:08
L2	12	execut\$3 and ((first test or test script\$1) and (mixed signal semiconductor device or mixed signal integrated circuit))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/13 17:20
L3		preparing execution same (second test or second test script\$1 or other test script\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/13 17:10
L4		preparing execut\$4 and (second test or second test script\$1 or other test script\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/13 17:10
L5	1	I2 and I4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/13 17:12
L6	4	(setting parameter\$1) same (next test\$1 or second test or different test script\$1 or other test script\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/13 17:20
L7	14	l4 or l6	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/13 17:16

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L8	0	processing test result near2 (first test or test script\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/13 17:17
L9	1087	process\$3 and (test result\$1 same (first test or test script\$1))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/13 17:19
L10		12 and 19	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/13 17:19
L11		19 and 17	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/13 17:19
L12	1098	I2 or I9	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/13 17:20
L13	1	I12 and I7	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/13 17:20
L14	853	(mixed signal semiconductor device\$1) or (mixed signal integrated circuit)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/13 17:35

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L15	1	l6 and 114	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON .	2007/04/13 17:21
L16	19419	single processor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/13 17:21
L17	1	I16 and I7	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/13 17:21
L18	5	116 and 19	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/13 17:34
L19	16642	mixed signal\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/13 17:34
L20	423444	digital signal\$1 or analog signal\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/13 17:35
L21	5965	l19 and l20	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON .	2007/04/13 17:35

L22	1190306	(semiconductor device\$1) or (integrated circuit)	US-PGPUB; USPAT; USOCR;	ADJ	ON	2007/04/13 17:35
			FPRS; EPO; JPO; DERWENT; IBM_TDB			
L23	2804	I21 and I22	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/13 17:35
L24	18	l23 and l12	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/13 17:36
L25	0	124 and 17	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/13 17:36
L26	1	124 and 118	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/13 17:36
L27	28777	714/724 or 714/725 or 714/733 or 714/734 or 714/740 or 714/11 or 714/10 or 714/38 or 714/776 or 714/799 or 714/48 or 714/30 or 365/201 or 714/? or 702/? or 365/?	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ .	ON .	2007/04/13 17:44
L28	5	I24 and I27	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/13 17:53

L29	1	116 and 128	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON .	2007/04/13 17:53
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